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By: RPZ

Date: 3/21/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Xiaopin Zhang et al.  
Applic. No. : 09/624,438  
Filed : July 24, 2000  
Title : Controllable Current Source Circuit and Phase Locked  
Loop Equipped Therewith  
Art Unit : 2787

Technology Center 2100

MAR 28 2001

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States Patent No. 5,119,043 (Borwn et al.), dated June 2, 1992;

United States Patent No. 5,453,680 (Giolma et al.), dated September 26, 1995;

United States Patent No. 5,465,061 (Dufour), dated November 7, 1995;

United States Patent No. 5,663,686 (Tada), dated September 2, 1997;

United States Patent No. 5,722,052 (Abdi et al.), dated February 24, 1998;

United States Patent No. 5,770,976 (Nagaraj), dated June 23, 1998;

European Patent Application EP 0 777 333 A1 (Nishikawa), dated June 4, 1997.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

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Date: March 21, 2001,

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